

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoichi YAMADA

Application No.: U.S. National Stage of PCT/JP00/02768

Filed: December 21, 2000

Docket No.: 107300

For: SEMICONDUCTOR INTEGRATED CIRCUIT

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office  
Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Page 19, line 11, change "the external memory 210" to --the external memory 212--.

IN THE CLAIMS:

Please amend claims 3-7, 10-13, and 16-20 as follows:

Claim 3, line 2, delete "or 2".

Claim 4, lines 1 and 2, change "any one of claims 1 to 3" to --claim 1--.

Claim 5, lines 1 and 2, change "any one of claims 1 to 4" to --claim 1--.

Claim 6, lines 1 and 2, change "any one of claims 1 to 5" to --claim 1--.

Claim 7, lines 1 and 2, change "any one of claims 1 to 6" to --claim 1--.

Claim 10, line 2, delete "or 9".

00720142 107300